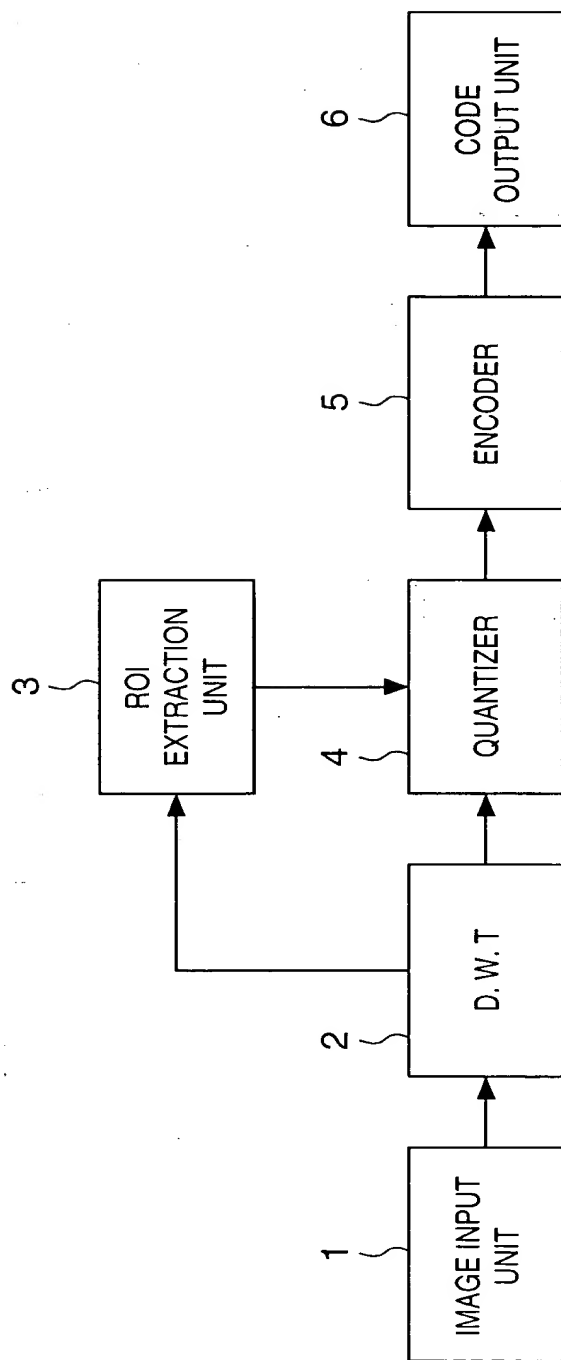


FIG. 1



The diagram illustrates a video signal processing system. It features two main processing paths: a horizontal path and a vertical path. The horizontal path, labeled 'HORIZONTAL' and '201', starts with an input signal that splits into two branches. The first branch passes through a Low Pass Filter (LPF) and a delay block (represented by a circle with a downward arrow and the number 2). The second branch passes through a High Pass Filter (HPF) and a delay block. These two branches are then combined. The vertical path, labeled 'VERTICAL' and '202', also starts with an input signal that splits into two branches. The first branch passes through a Low Pass Filter (LPF) and a delay block. The second branch passes through a High Pass Filter (HPF) and a delay block. These two branches are then combined. The outputs of the horizontal and vertical paths are then combined to produce the final output signals, labeled HL1, LH1, HL2, and HH2.

FIG. 2B

LL	HL2	HL1
LH2	HH2	
LH1		HH1

09635000 100600

FIG. 3

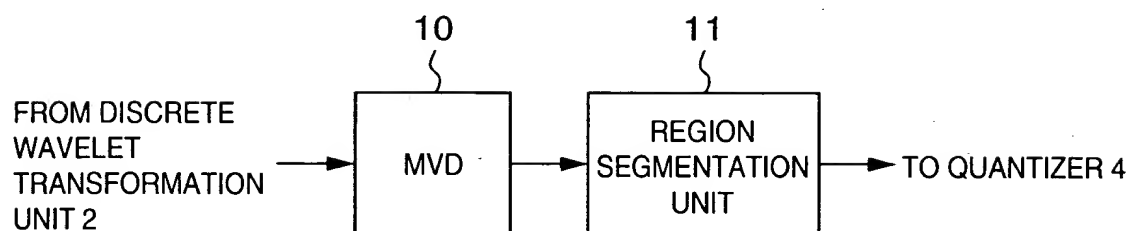
[illegible]

FIG. 4

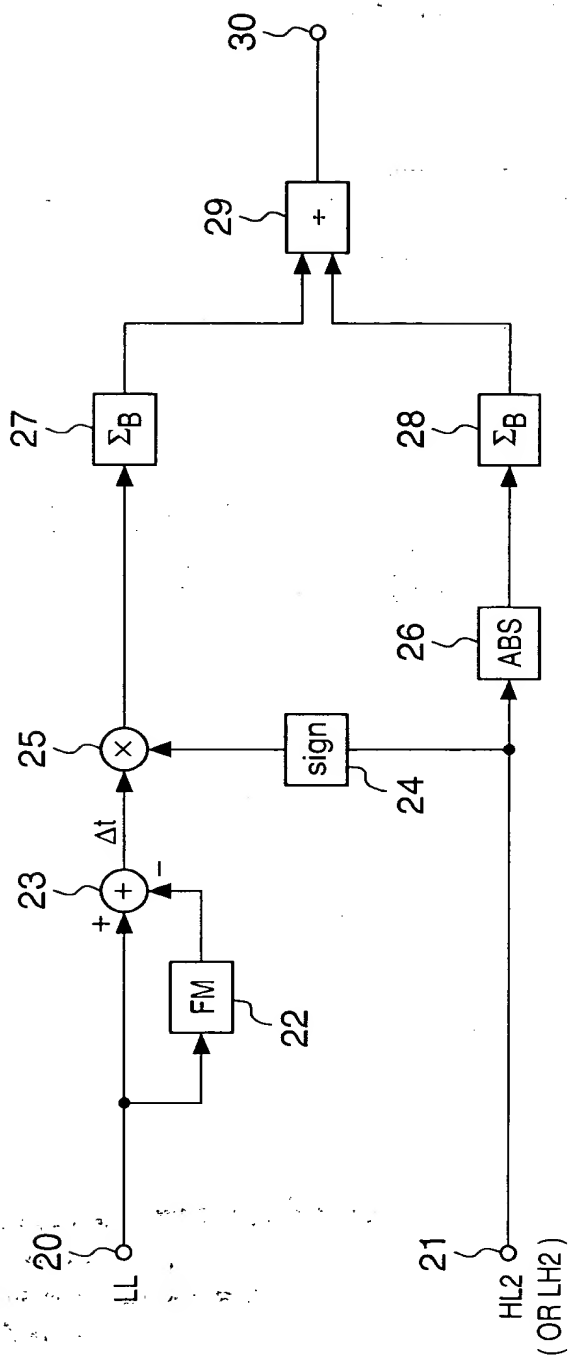


FIG. 5A

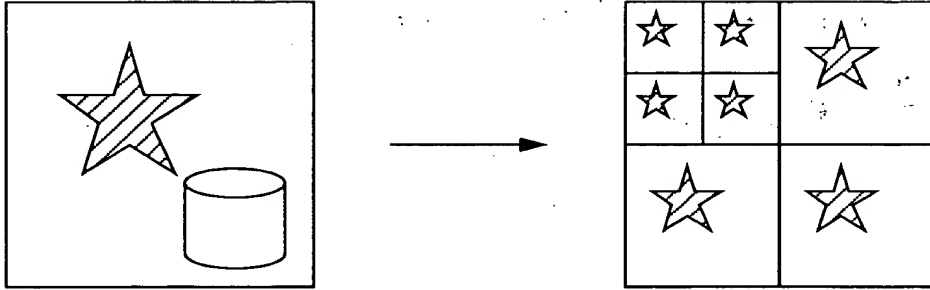


FIG. 5B

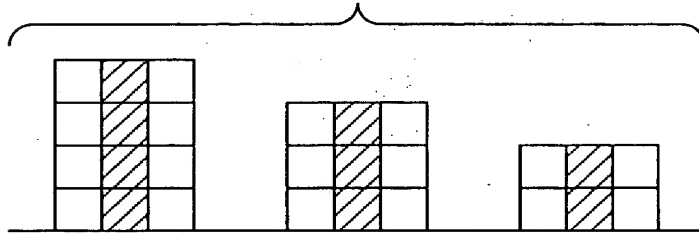


FIG. 5C

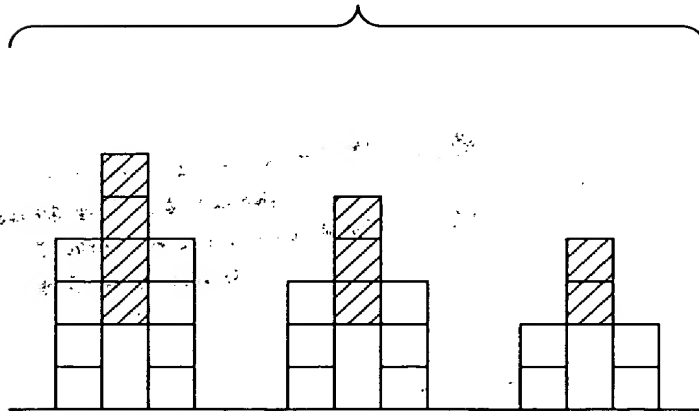


FIG. 6

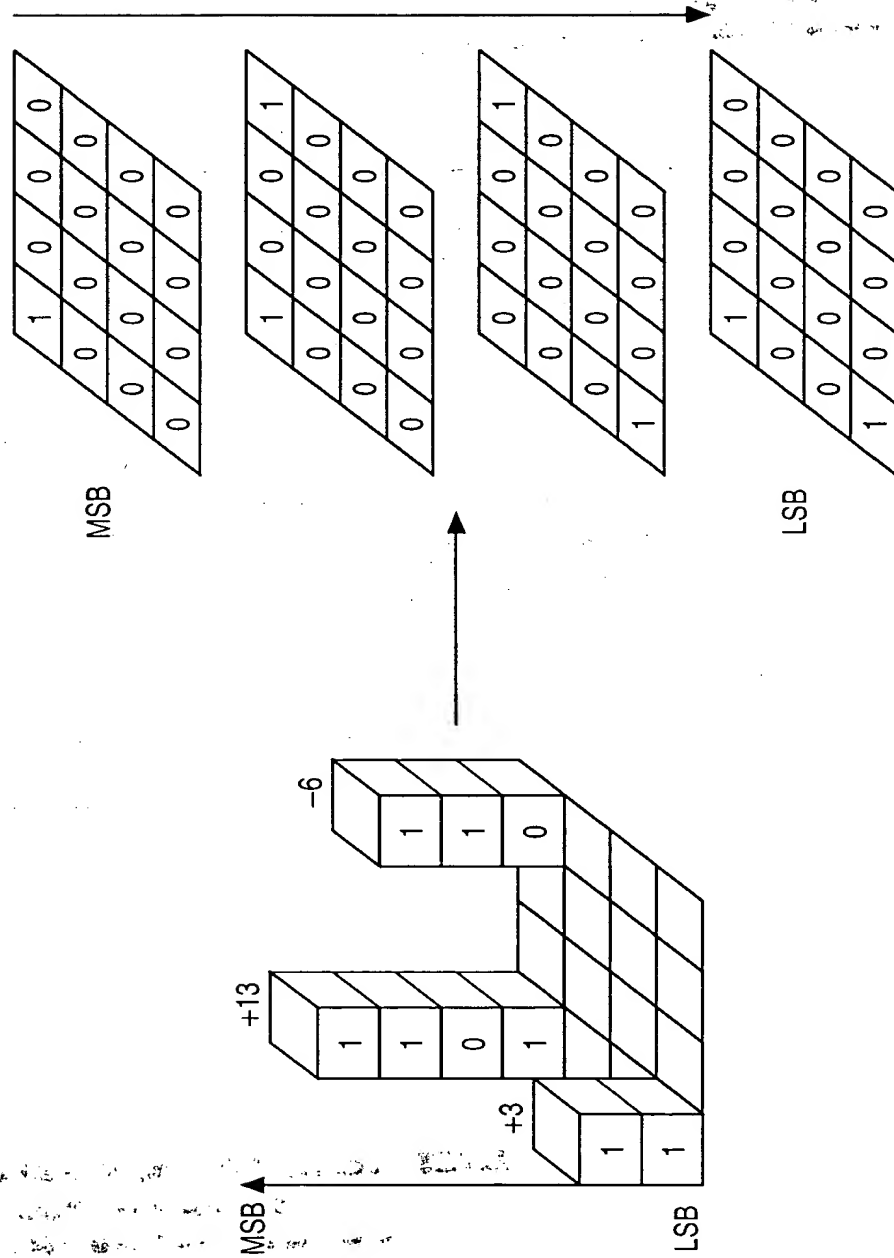


FIG. 7

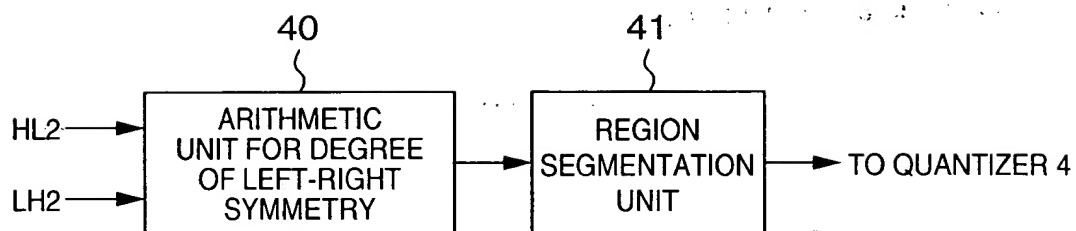


FIG. 8

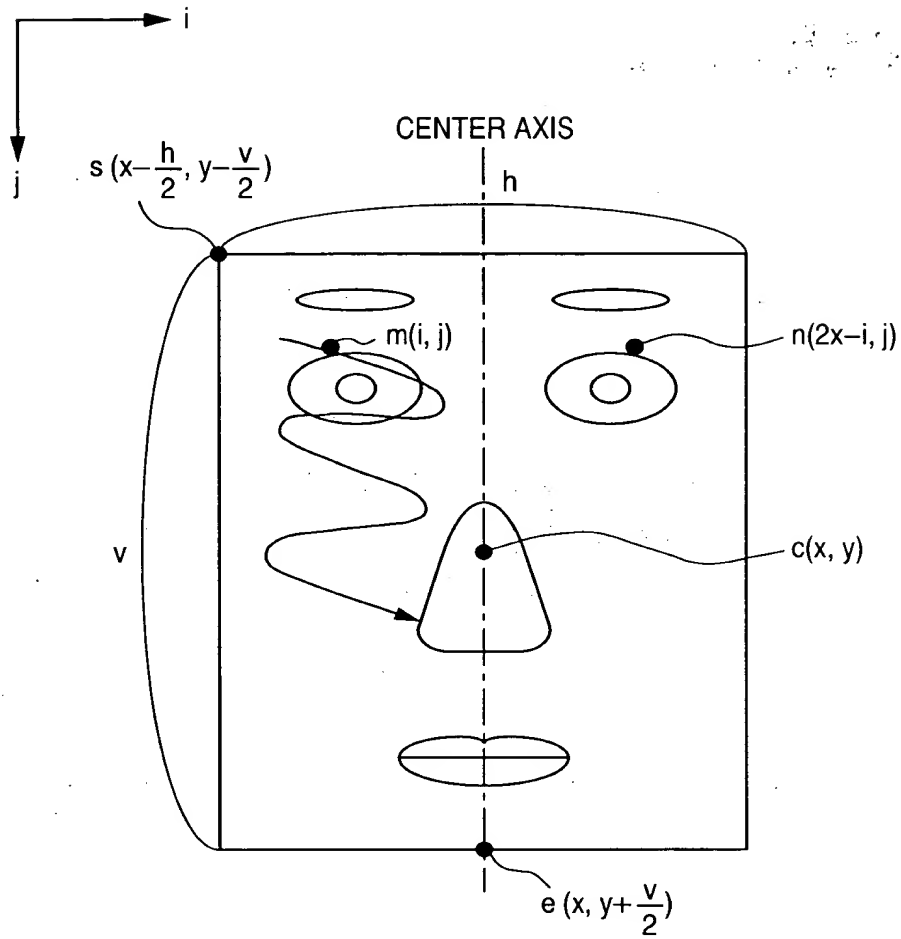


FIG. 9

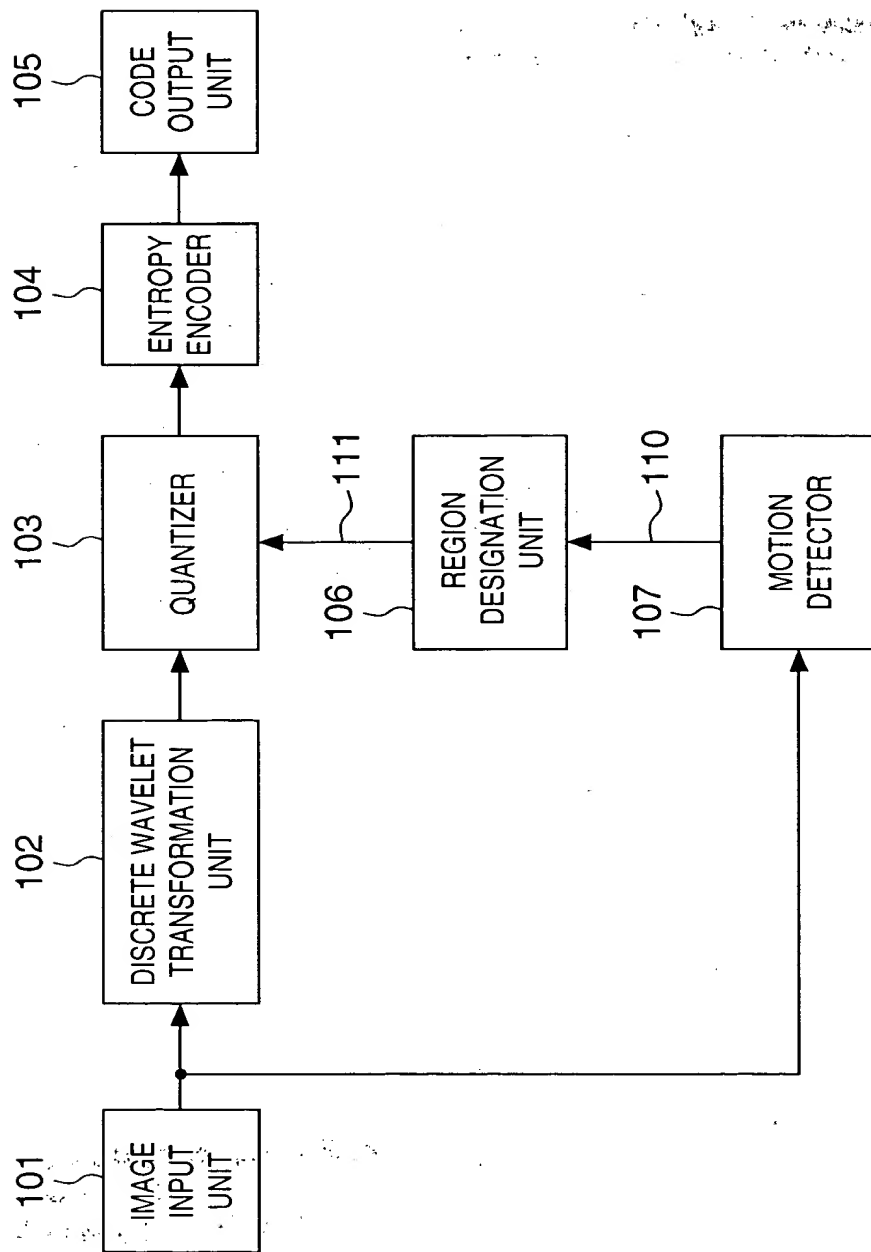
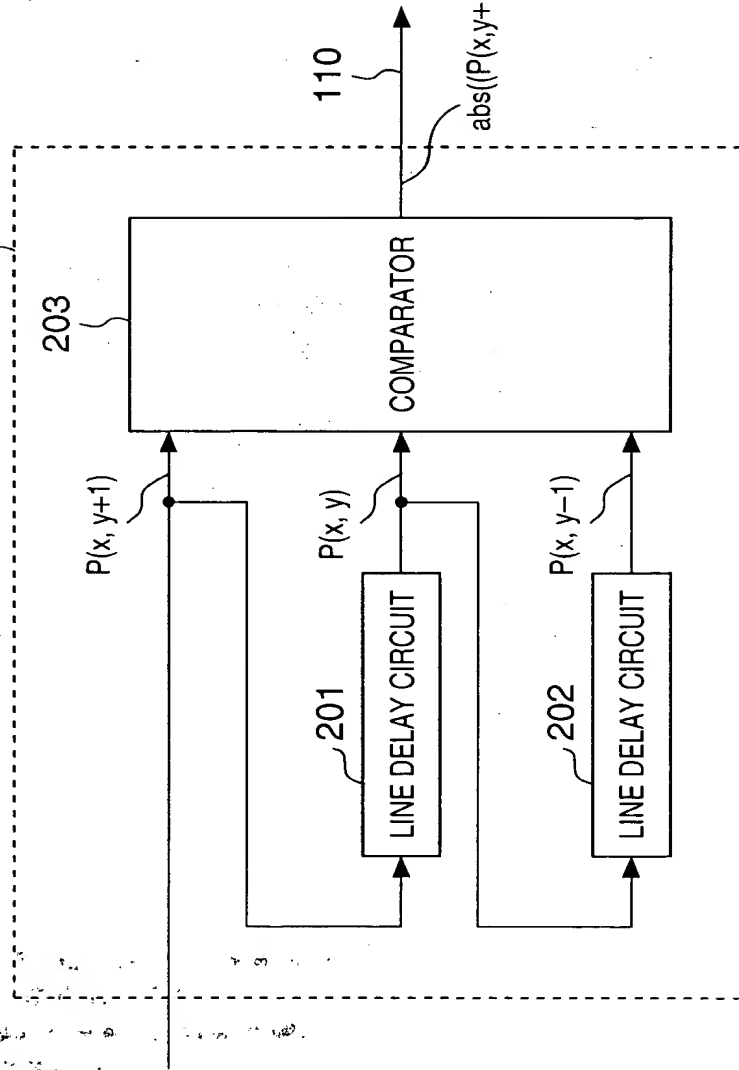


FIG. 10¹⁰⁷



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COMPARATOR

 $P(x, y)$ $Q(x, y)$

FRAME DELAY CIRCUIT

$$\text{abs}((Q(x,y)-P(x,y))>k$$

110

FIG. 12

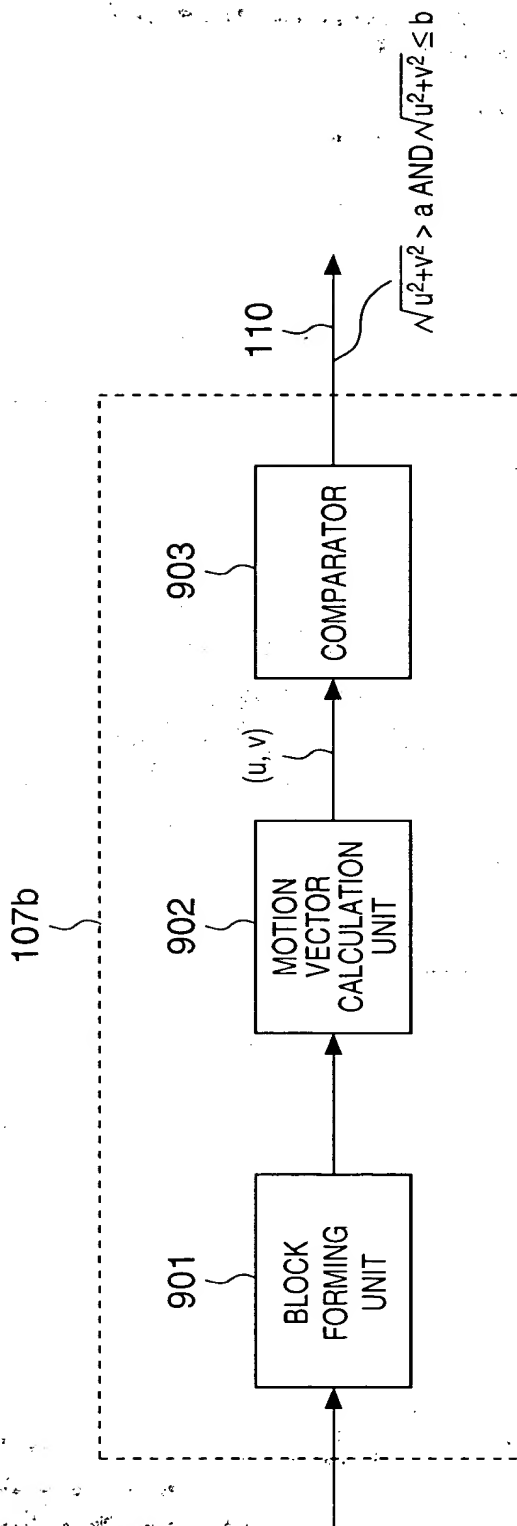


FIG. 13

